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**SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT**

Docket Number: NOV 16 1998
2885/14A
Group 2700

Application Number
09/145,139

Filing Date
August 28, 1998

Examiner
Not yet assigned

Art Unit
2781

Invention Title

INTERNAL BUS SYSTEM FOR DFPS AND UNITS
WITH TWO- OR MULTI-DIMENSIONAL
PROGRAMMABLE CELL ARCHITECTURES, FOR
MANAGING LARGE VOLUMES OF DATA WITH A
HIGH INTERCONNECTION COMPLEXITY

Inventor(s)

VORBACH et al.

Assistant Commissioner
for Patents
Washington D.C. 20231

I hereby certify that this correspondence is being deposited with the
United States Postal Service as first class mail in an envelope addressed
to: Assistant Commissioner for Patents, Washington, D.C. 20231 on

Date: 9 Nov 1998

Reg. No. 36,098

Signature: _____

Michelle M. Carniaux

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicant hereby bring the following references to the attention of the Examiner. These references are listed on the attached modified PTO Form No. 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed.

Dated: 9 Nov 1998

By: _____

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